

APR 14 2006

SHEET 1 OF 3

INFORMATION DISCLOSURE CITATION

PTO-1449

ATTY. DOCKET NO.
SUN-P9329APPLICATION NO.
10/637,167APPLICANT
Marc Tremblay, et al.FILING DATE
08 August 2003GROUP ART UNIT
2188

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
KP	6,460,124	10-2002	Kagi et al.	711	163	
KP	2002/0087810	07-2002	Boatright et a.	711	145	
KP	2004/0186970	09-2004	Kekre et al.	711	62	
KP	2004/0162948	08-2004	Tremblay et al.	711	137	
KP	2003/066056	04-2003	Petersen et al.	717	137	
KP	6,578,088	06-2003	Singhal et al.	707	8	
KP	6,360,220	03-2002	Forin	707	8	
KP	5,940,827	08-1999	Hapner et al	707	8	

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

KP	Publication entitled "Speculation-Based Techniques for Transactional Lock-Free Execution of Lock-Based Programs", by Ravi Rajwar, Online! 2002, XP002286237, Retrieved from the internet: URL: http://bcr.uwaterloo.ca/{brecht/courses/856/readings-new/rajwar02speculationsbased.pdf
KP	Publication entitled "Checkpoint Processing and Recovery: Towards Scalable Large Instruction Window Processors", by Haitham Akkary et al., Proceedings of the 36 th International Symposium on Microarchitecture, 2003, IEEE
KP	Publication entitled "Multiple Reservations and the Oklahoma Update", by Janice M. Stone et al., IEEE Parallel & Distributed Technology, November 1993, Pages 58-71.

EXAMINER

/Kaushikkumar Patel/

DATE CONSIDERED
(08/10/2006)

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
KP	5,428,761	06-1995	Herlihy et al.	711	130		
KP	5,835,764	11-1998	Platt et al.	718	101		
KP	5,742,785	04-1998	Stone et al.	712	217		
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
KP	Publication entitled "Improving the Throughput of Synchronization by Insertion of Delays", by Ravi Rajwar et al. Proceedings of the Sixth International Symposium on High-Performance Computer Architecture, 8-12 January 2000, Pages 168-179.						
KP	Publication entitled "Checkpoint Processing and Recovery: An Efficient, Scalable Alternative to Reorder Buffers" Haitham Akkary et al. IEEE Computer Society, November-December 2003, Pages 11-19.						
KP	Publication entitled "Multi-view Memory to Support OS Locking For Transaction Systems", P. Bodorik et al., IEEE, 1997, Pages 309-318.						
KP	Publication entitled "Indexing for Multiversion Locking: Alternatives and Performance Evaluation", Paul M. Bober et al, IEEE Transactions on Knowledge and Data Engineering, Vol. 9, No1, January-February 1997, Pages 68-84.						
KP	Publication entitled: "Transactional Execution: Toward Reliable, High-Performance Multithreading" by Ravi Rajwar et al. IEEE Computer Society, November-December 2003, Pages 117-125						
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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
KP	6,021,480	03-2002	Petty	711	201		
KP	6,918,012	07-2005	Venkitakrishnan et al	711	150		
KP	5,701,432	12-1997	Wong et al.	711	130		
KP	5,974,438	10-1999	Neufeld	718	104		
KP	6,185,577	02-2001	Nainani et al.				
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
KP	Publication: "Speculative Synchronization: Applying Thread-Level Speculation to Explicitly Parallel Applications" by Jose F. Martinez and Josep Torrellas, Dept. of Computer Science, University of Illinois at Urbana-Champaign, Urbana, IL 61801 USA, http://iacoma.cs.uiuc.edu , XP-002285169, published in ASPLOS X, 10-2002, Pgs. 18-29						
KP	Publication: "Transactional Memory: Architectural Support for Lock-Free Data Structures" by Maurice Herlihy, Digital Equip. Corp. Cambridge Research Laboratory, Cambridge, MA 02139, herlihy@crl.dec.com and J. Eliot B. Moss, Dept. of Computer Science, University of Massachusetts, Amherst, MA 01003, moss@cs.umass.edu , XP-000380375, published in Computer Architecture News, May 21, 1993, Pgs. 289-300						
KP	Publication: "Enhancing Software Reliability with Speculative Threads" by Jeffrey Oplinger and Monica S. Lam, Computer Systems, Laboratory, Stanford University, jeffop@stanford.edu , XP-002285168 published in SPLOS X, 10-2202, Pgs. 184-196						
KP	Publication: "Transactional Memory: Architectural Support for Lock-Free Data Structures" by Maurice Herlihy, Digital Equip. Corp. Cambridge Research Laboratory, Cambridge, MA 02139, herlihy@crl.dec.com and J. Eliot B. Moss, Dept. of Computer Science, University of Massachusetts, Amherst, MA 01003, moss@cs.umass.edu , XP-000380375, published in Computer Architecture News, May 21, 1993, Pgs. 289-300						
EXAMINER /Kaushikkumar Patel/ (08/10/2006)				DATE CONSIDERED 10/2006			

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